Course code | Course Name | L-T-P - Credits | Year of Introduction
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CS431 | COMPILER DESIGN LAB | 0-0-3-1 | 2016

Pre-requisite : CS331 System Software Lab

Course Objectives:
- To implement the different Phases of compiler.
- To implement and test simple optimization techniques.
- To give exposure to compiler writing tools.

List of Exercises/Experiments:
1. Design and implement a lexical analyzer for given language using C and the lexical analyzer should ignore redundant spaces, tabs and new lines.
2. Implementation of Lexical Analyzer using Lex Tool
3. Generate YACC specification for a few syntactic categories.
   a) Program to recognize a valid arithmetic expression that uses operator +, -, *, and /.
   b) Program to recognize a valid variable which starts with a letter followed by any number of letters or digits.
   c) Implementation of Calculator using LEX and YACC
   d) Convert the BNF rules into YACC form and write code to generate abstract syntax tree
4. Write program to find ε – closure of all states of any given NFA with ε transition.
5. Write program to convert NFA with ε transition to NFA without ε transition.
6. Write program to convert NFA to DFA
7. Write program to minimize any given DFA.
8. Develop an operator precedence parser for a given language.
9. Write program to find Simulate First and Follow of any given grammar.
10. Construct a recursive descent parser for an expression.
11. Construct a Shift Reduce Parser for a given language.
12. Write a program to perform loop unrolling.
13. Write a program to perform constant propagation.
15. Implement the back end of the compiler which takes the three address code and produces the 8086 assembly language instructions that can be assembled and run using an 8086 assembler. The target assembly instructions can be simple move, add, sub, jump etc.

Expected Outcome:
The Student will be able to:
- Implement the techniques of Lexical Analysis and Syntax Analysis.
- Apply the knowledge of Lex & Yacc tools to develop programs.
- Generate intermediate code.
- Implement Optimization techniques and generate machine level code.